

Amendments to the Abstract

Please **amend** the Abstract to read.

-- Response fidelity problems appear for some specific video levels at PDP borders. The reason is that some cells at the border of the PDP panel are not completely closed and pollute when switched ON neighbouring cells being OFF. Therefore, it is suggested to encode the video levels in the border area in a specific way. Especially, for critical sub-fields within the code it is forbidden to insert a binary 0 between two binary 1. Thus, the neighbourhood of critical sub-fields being ON and OFF is avoided. Preferably, the specific border coding is performed under the control of an average power management (2) and codewords being not used are recreated by dithering (6). --